

Dual N-Channel Enhancement Mode MOSFET

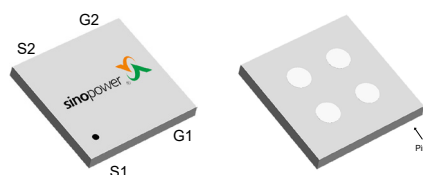
Features

- 24V/10A
 - $R_{SS(ON)} = 18.8\text{m}\Omega(\text{max.}) @ V_{GS} = 4.5\text{V}$
 - $R_{SS(ON)} = 19.4\text{m}\Omega(\text{max.}) @ V_{GS} = 4\text{V}$
 - $R_{SS(ON)} = 20.3\text{m}\Omega(\text{max.}) @ V_{GS} = 3.7\text{V}$
 - $R_{SS(ON)} = 21.9\text{m}\Omega(\text{max.}) @ V_{GS} = 3.1\text{V}$
 - $R_{SS(ON)} = 24.3\text{m}\Omega(\text{max.}) @ V_{GS} = 2.5\text{V}$
- Reliable and Rugged
- ESD Protection
- Lead Free and Green Devices Available (RoHS Compliant)

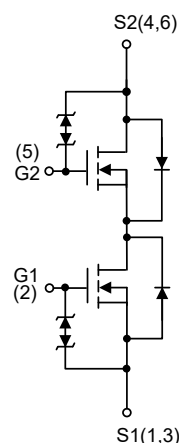
Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.
- One Cell Li-ion Battery Pack.

Pin Description



WLCSP-A_1.75x1.75-4



N-Channel MOSFET

Ordering and Marking Information

SM9982DS □□□-□□□ Assembly Material Handling Code Temperature Range Package Code	Package Code HE : WLCSP-A_1.75x1.75-4 Operating Junction Temperature Range C : -55 to 150 °C Handling Code TR : Tape & Reel Assembly Material G : Halogen and Lead Free Device
SM9982DS HE : 9982I XXXXX 	XXXXX - Lot Code

Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ Unless Otherwise Noted)

Symbol	Parameter		Rating	Unit
Common Ratings				
V _{SSS}	Source to Source Voltage		24	V
V _{GSS}	Gate-Source Voltage		±12	
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to 150	
I _S	Source Current	T _A =25°C	10	A
		T _A =70°C	7.8	
I _{SM} ^b	Pulsed Source Current	T _A =25°C	39	
P _D	Maximum Power Dissipation	T _A =25°C	2.8	W
		T _A =70°C	1.8	
R _{θJA} ^a	Thermal Resistance-Junction to Ambient	t ≤ 10s	45	°C/W
		Steady state	65	
I _{AS} ^{c, T8}	Avalanche Current, Single pulse	L=0.1mH	22	A
E _{AS} ^c	Avalanche Energy, Single pulse	L=0.1mH	24.2	mJ

Note a : When mounted on ceramic substrate (50mm*100mm*1mm).

Note b : Pulse width limited by maximum junction temperature.

Note c : UIS tested and pulse width limited by maximum junction temperature (initial temperature $T_J=25^{\circ}\text{C}$).

Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

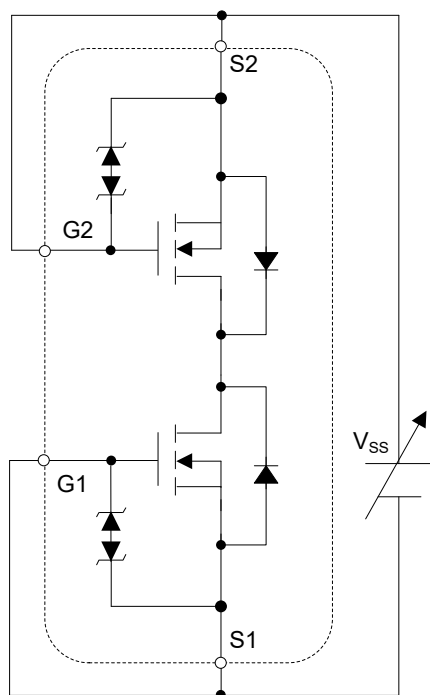
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV_{SSS}^{T1}	Source-Source Breakdown Voltage	$V_{GS}=0V, I_{SS}=250\mu A$	24	-	-	V
I_{SSS}^{T1}	Zero Gate Voltage Source Current	$V_{SS}=19.2V, V_{GS}=0V$	-	-	1	μA
$V_{GS(th)}^{T2}$	Gate Threshold Voltage	$V_{GS}=V_{SS}, I_{SS}=250\mu A$	0.5	0.9	1.3	V
I_{GSS}^{T3}	Gate Leakage Current	$V_{GS}=\pm 12V, V_{SS}=0V$	-	-	± 10	μA
$R_{SS(ON)}^{d,T4}$	Source -Source On-state Resistance	$V_{GS}=4.5V, I_{SS}=2A$	11.3	15	18.8	m Ω
		$V_{GS}=4V, I_{SS}=2A$	11.2	15.3	19.4	
		$V_{GS}=3.7V, I_{SS}=2A$	10.9	15.6	20.3	
		$V_{GS}=3.1V, I_{SS}=2A$	11.1	16.5	21.9	
		$V_{GS}=2.5V, I_{SS}=2A$	11.7	18	24.3	
Diode Characteristics						
$V_{SS}^{d,T5}$	Diode Forward Voltage	$I_{SS}=2A$	-	0.7	1.1	V
Dynamic Characteristics ^e						
C_{iss}	Input Capacitance	$V_{GS}=0V,$ $V_{SS}=12V,$ Frequency=1.0MHz	-	2380	3094	pF
C_{oss}	Output Capacitance		-	180	-	
C_{rss}	Reverse Transfer Capacitance		-	100	-	
$t_{d(ON)}^{T6}$	Turn-on Delay Time	$V_{SS}=12V, R_L=12\Omega,$ $I_{SS}=1A, V_{GEN}=10V,$ $R_G=6\Omega$	-	13	24	ns
t_r^{T6}	Turn-on Rise Time		-	14	26	
$t_{d(OFF)}^{T6}$	Turn-off Delay Time		-	85	153	
t_f^{T6}	Turn-off Fall Time		-	23	42	
Gate Charge Characteristics ^{e,T7}						
Q_g	Total Gate Charge	$V_{SS}=12V, V_{GS}=4.5V,$ $I_{SS}=2A$	-	22.5	31.5	nC
Q_{gth}	Threshold Gate Charge		-	1.9	-	
Q_{gs}	Gate-Source Charge		-	3.1	-	
Q_{gd}	Gate-Drain Charge		-	4.7	-	

Note d : Pulse test ; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

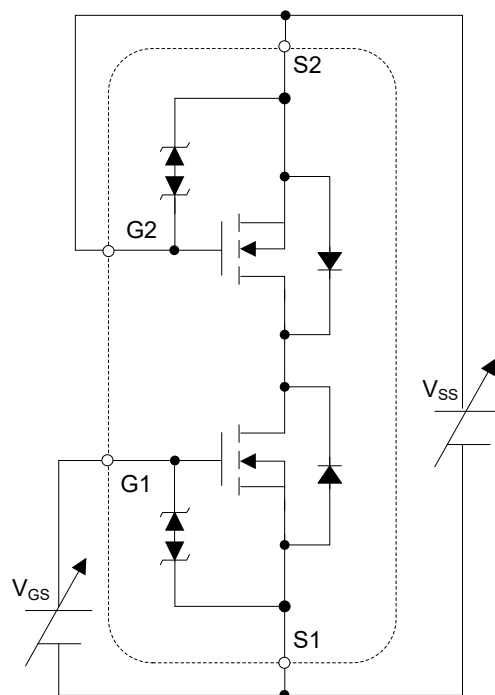
Note e : Guaranteed by design, not subject to production testing.

Note T1-T8 : Test Circuit 1-8.

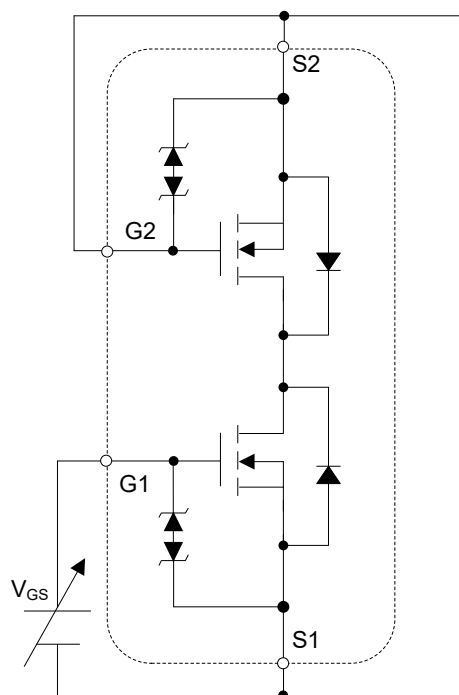
Test Circuit 1 (BV_{SSS} , I_{SSS})



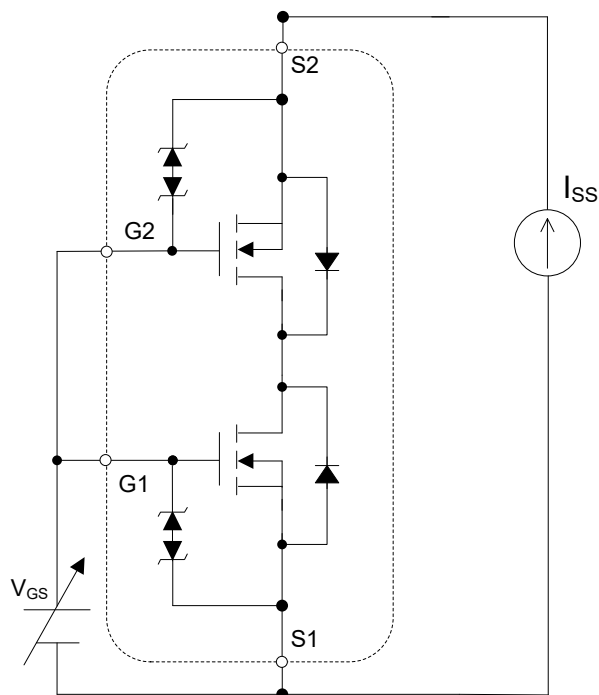
Test Circuit 2 ($V_{GS(th)}$)



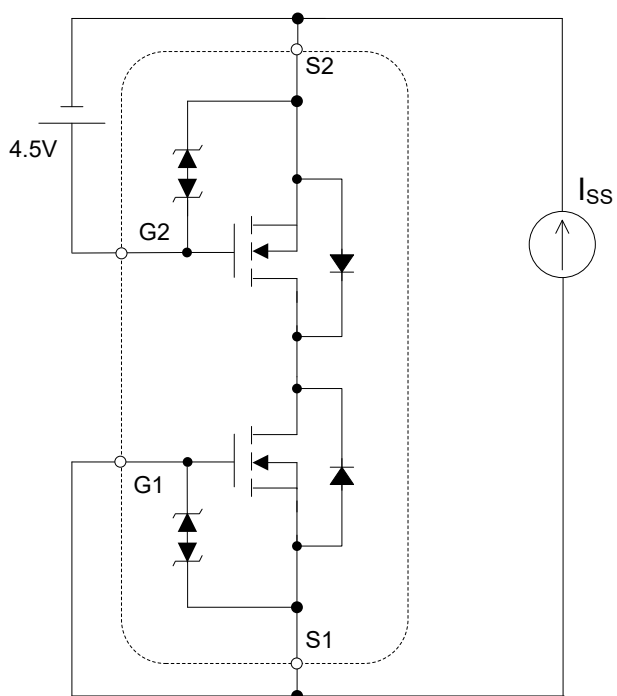
Test Circuit 3 (I_{GSS})



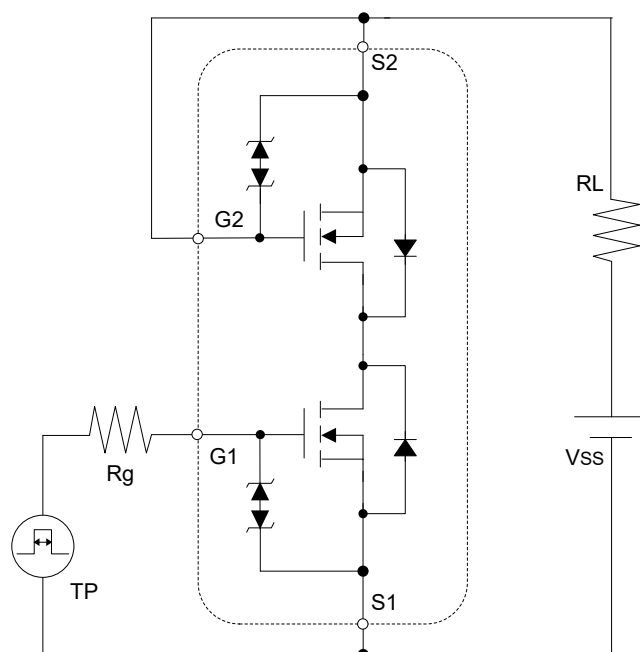
Test Circuit 4 ($R_{SS(ON)}$)



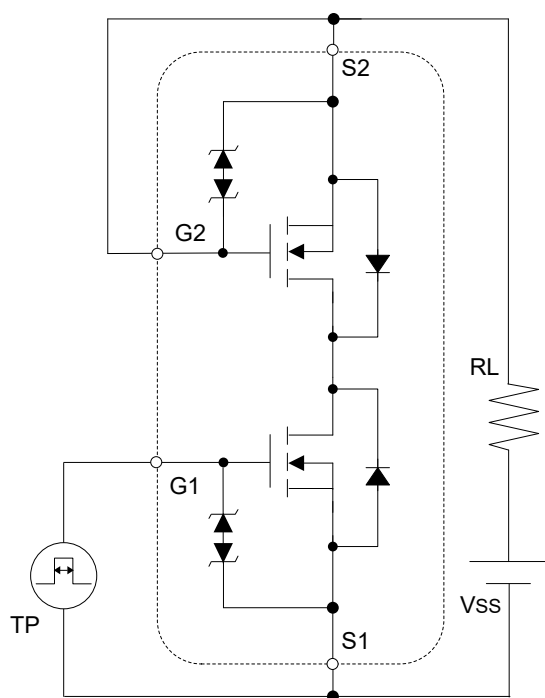
Test Circuit 5 (V_{ss})



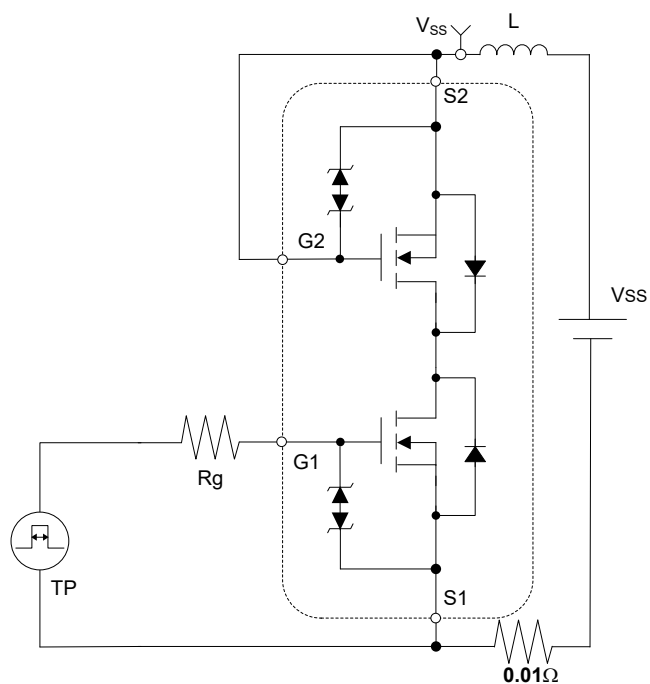
Test Circuit 6 (Switching time)



Test Circuit 7 (Gate Charge)

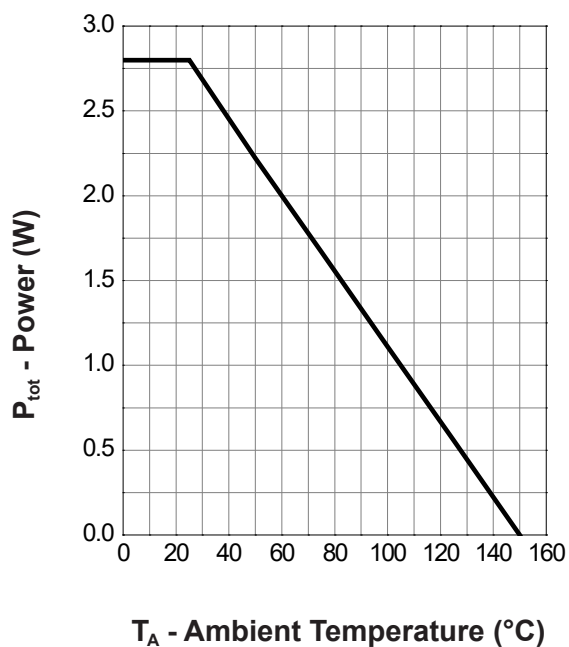


Test Circuit 8 (Avalanche test)

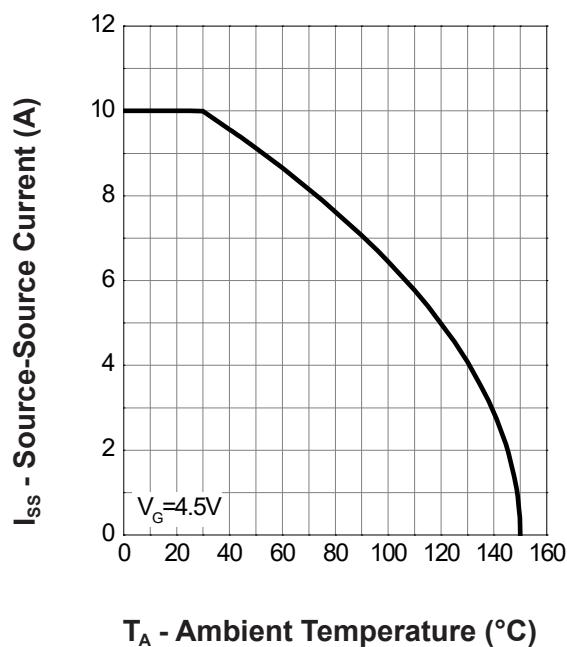


Typical Operating Characteristics

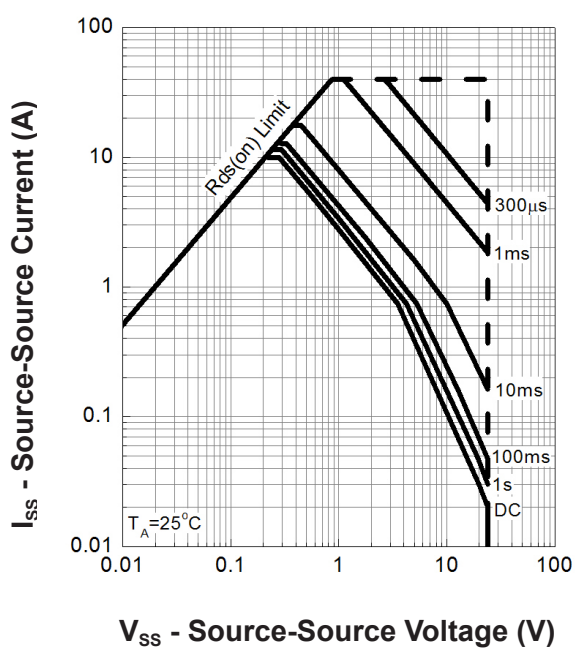
Power Dissipation



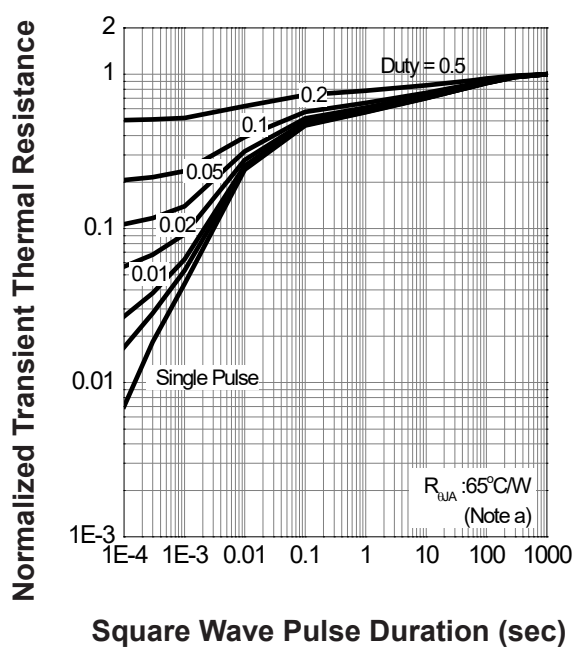
Source-Source Current



Safe Operation Area

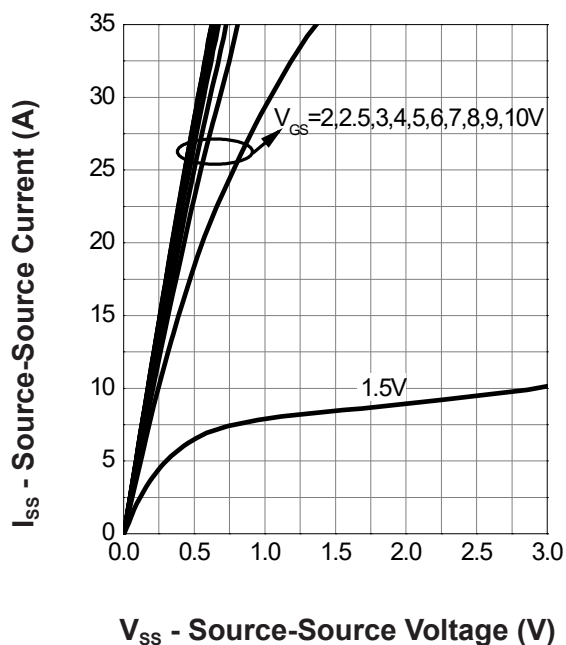


Thermal Transient Impedance

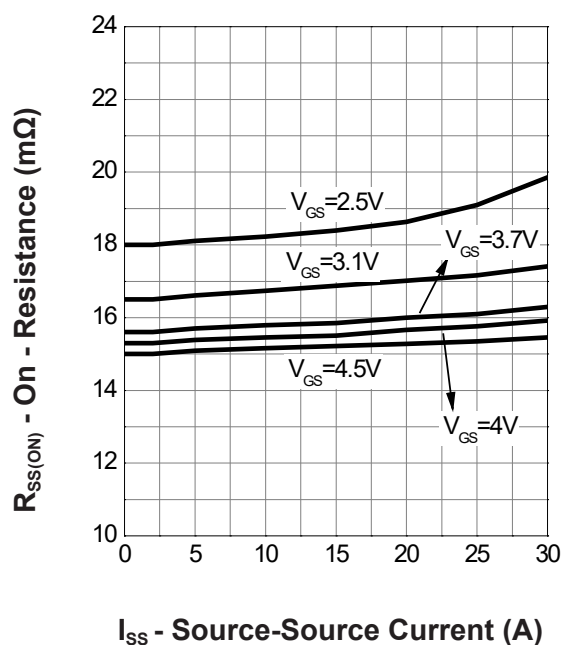


Typical Operating Characteristics(Cont.)

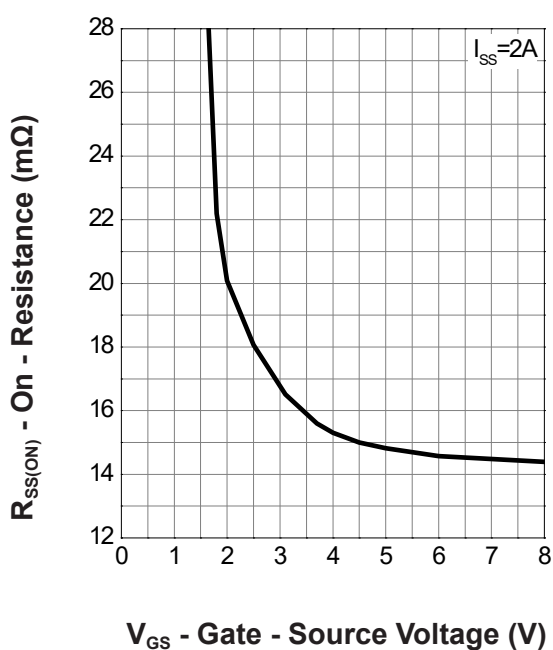
Output Characteristics



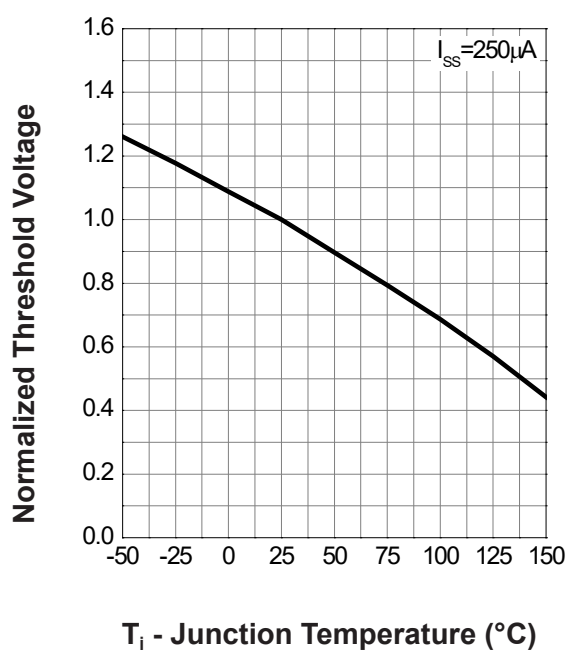
Source-Source On Resistance



Gate-Source On Resistance

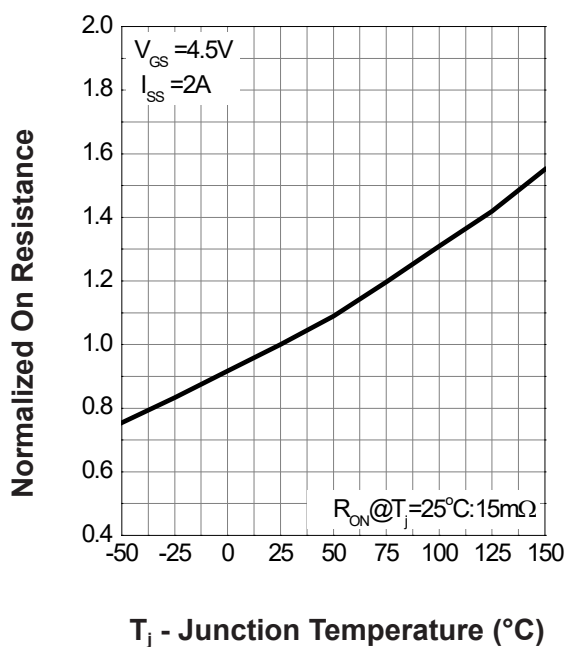


Gate Threshold Voltage

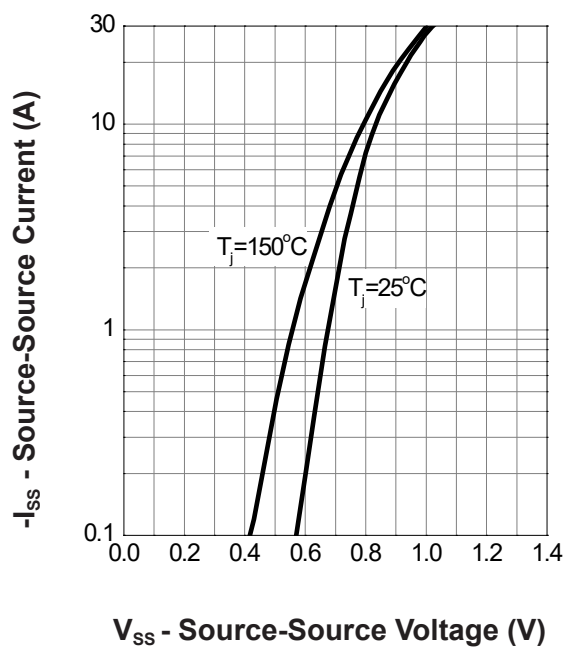


Typical Operating Characteristics(Cont.)

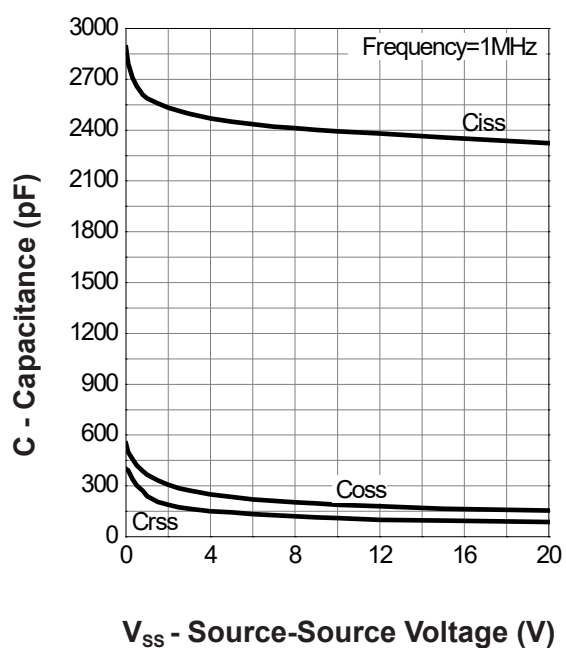
Source-Source On Resistance



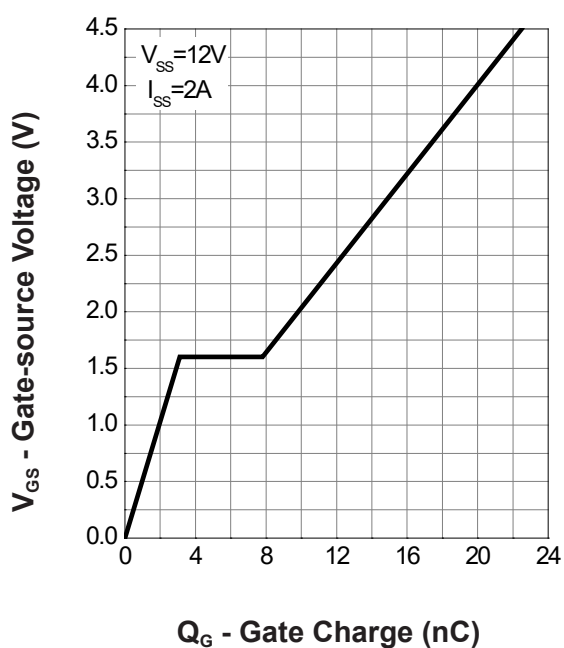
Source-Source Diode Forward



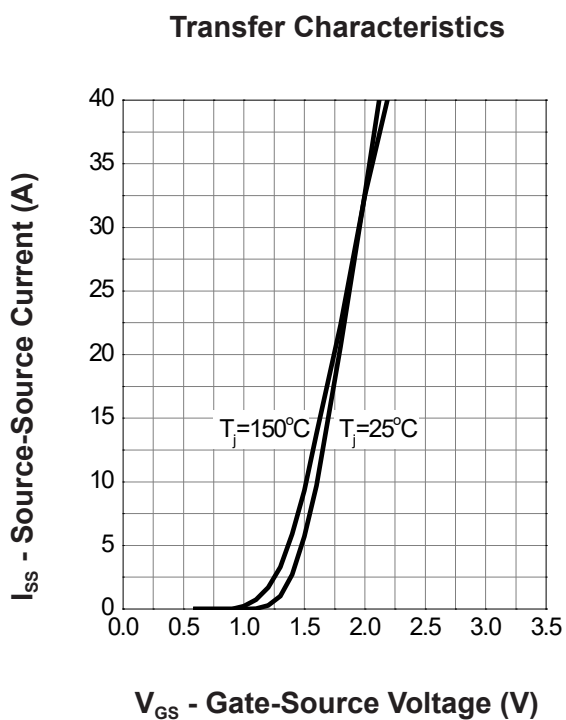
Capacitance



Gate Charge



Typical Operating Characteristics(Cont.)



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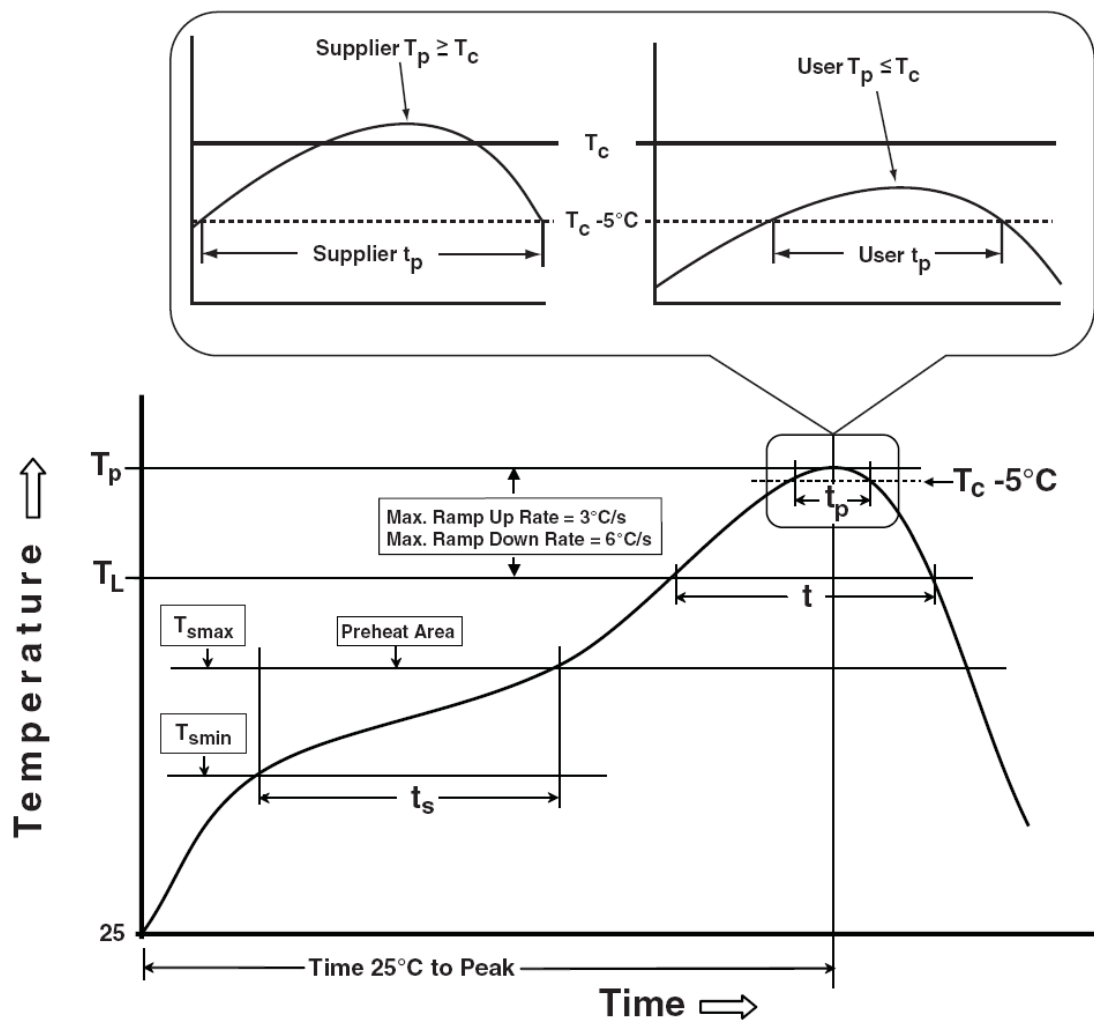
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Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T_{smin})	100 °C	150 °C
Temperature max (T_{smax})	150 °C	200 °C
Time (T_{smin} to T_{smax}) (t_s)	60-120 seconds	60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L)	183 °C	217 °C
Time at liquidous (t_L)	60-150 seconds	60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum.		
** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ T_{jmax}
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ T_{jmax}
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

Customer Service

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