

Dual N-Channel Enhancement Mode MOSFET

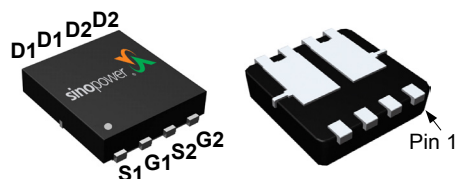
Features

- 100V/7A
 $R_{DS(ON)} = 162m\Omega(max.) @ V_{GS} = 10V$
 $R_{DS(ON)} = 180m\Omega(max.) @ V_{GS} = 4.5V$
- 100% UIS + R_g Tested
- ESD Protection
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

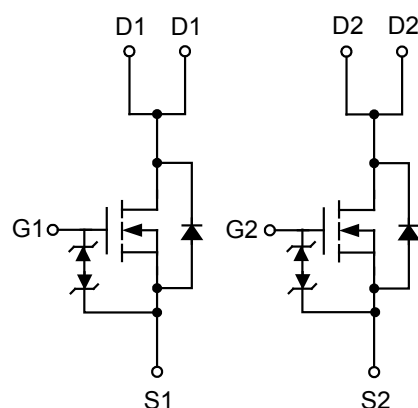
Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.

Pin Description



DFN5x6C-8_EP2



N-Channel MOSFET

Ordering and Marking Information

SM7310DS XXXXXXXX Assembly Material Handling Code Temperature Range Package Code	Package Code KP : DFN5x6C-8_EP2 Operating Junction Temperature Range C : -55 to 150 °C Handling Code TR : Tape & Reel Assembly Material G : Halogen and Lead Free Device
SM7310DS KP :	 7310DS XXXXX XXXXX - Lot Code

Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the leadfree requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ Unless Otherwise Noted)

Symbol	Parameter		Rating	Unit
V_{DSS}	Drain-Source Voltage		100	V
V_{GSS}	Gate-Source Voltage		± 20	
T_J	Maximum Junction Temperature		150	C
T_{STG}	Storage Temperature Range		-55 to 150	
I_S	Diode Continuous Forward Current	$T_C=25^{\circ}\text{C}$	3.5	A
I_D	Continuous Drain Current	$T_C=25^{\circ}\text{C}$	7	
		$T_C=100^{\circ}\text{C}$	4.4	
I_{DM}^{a}	Pulse Drain Current	$T_C=25^{\circ}\text{C}$	28	A
P_D	Maximum Power Dissipation	$T_C=25^{\circ}\text{C}$	17.8	W
		$T_C=100^{\circ}\text{C}$	7.1	
$R_{\theta\text{JC}}$	Thermal Resistance-Junction to Case	Steady State	7	$^{\circ}\text{C/W}$
I_D	Continuous Drain Current	$T_A=25^{\circ}\text{C}$	1.8	A
		$T_A=70^{\circ}\text{C}$	1.5	
P_D	Maximum Power Dissipation	$T_A=25^{\circ}\text{C}$	1.25	W
		$T_A=70^{\circ}\text{C}$	0.8	
$R_{\theta\text{JA}}^{\text{b}}$	Thermal Resistance-Junction to Ambient	Steady State	100	$^{\circ}\text{C/W}$
I_{AS}^{c}	Avalanche Current, Single pulse	$L=0.5\text{mH}$	5	A
E_{AS}^{c}	Avalanche Energy, Single pulse	$L=0.5\text{mH}$	6.25	mJ

Note a : Pulse width is limited by max. junction temperature.

Note b : Surface Mounted on 1in^2 pad area, steady state $t = 999\text{s}$.

Note c : UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature $T_J=25^{\circ}\text{C}$).

Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

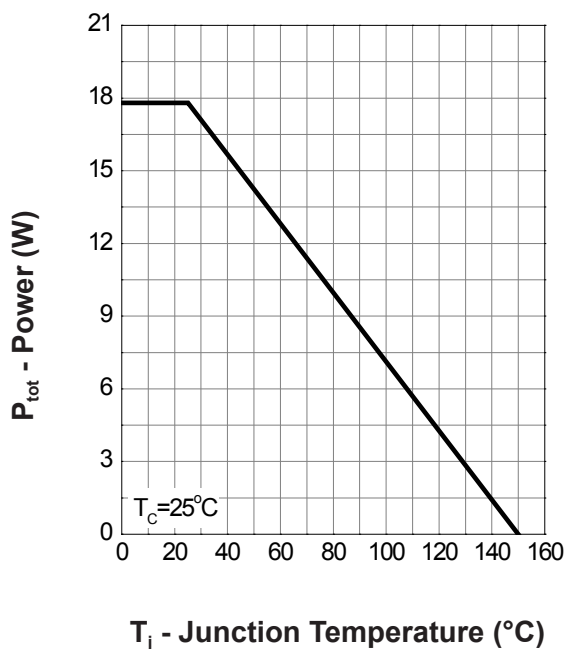
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	100	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =80V, V _{GS} =0V T _J =85°C	-	-	1 30	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	1	2	3	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±10	μA
R _{DS(ON)} ^d	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =4A V _{GS} =4.5V, I _{DS} =3A	-	135 138	162 180	mΩ
Diode Characteristics						
V _{SD} ^d	Diode Forward Voltage	I _{SD} =2A, V _{GS} =0V	-	0.8	1.3	V
t _{rr}	Reverse Recovery Time	V _{dd} =4V, dI _{SD} /dt=100 A/μs	-	42	-	ns
Q _{rr}	Reverse Recovery Charge		-	38	-	nC
Dynamic Characteristics ^e						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	-	2.5	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =30V, Frequency=1.0MHz	-	440	570	pF
C _{oss}	Output Capacitance		-	30	-	
C _{rss}	Reverse Transfer Capacitance		-	16	-	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =30V, R _L =30Ω, I _{DS} =1A, V _{GEN} =10V, R _G =6Ω	-	9	17	ns
t _r	Turn-on Rise Time		-	7	13	
t _{d(OFF)}	Turn-off Delay Time		-	18	33	
t _f	Turn-off Fall Time		-	4	8	
Gate Charge Characteristics ^e						
Q _g	Total Gate Charge	V _{DS} =50V, V _{GS} =4.5V, I _{DS} =4A	-	5	-	nC
Q _g	Total Gate Charge	V _{DS} =50V, V _{GS} =10V, I _{DS} =4A	-	10	14	
Q _{gs}	Gate-Source Charge		-	1.7	-	
Q _{gd}	Gate-Drain Charge		-	2.1	-	

Note d : Pulse test ; pulse width $\leq$ 300ms, duty cycle $\leq$ 2%.

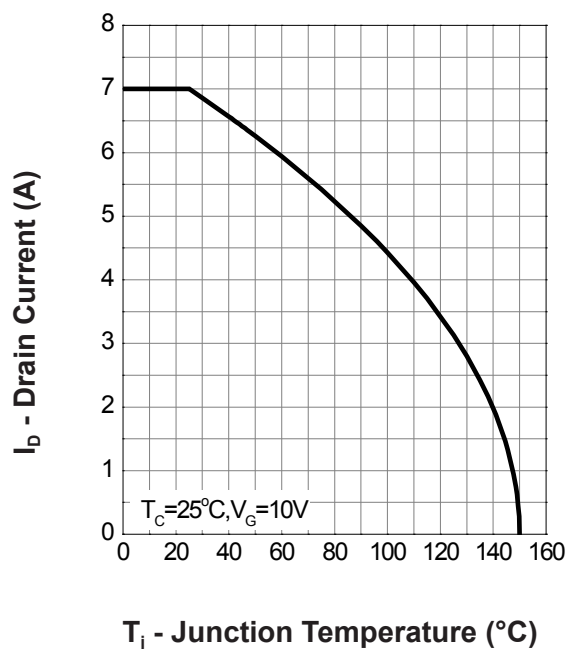
Note e : Guaranteed by design, not subject to production testing.

Typical Operating Characteristics

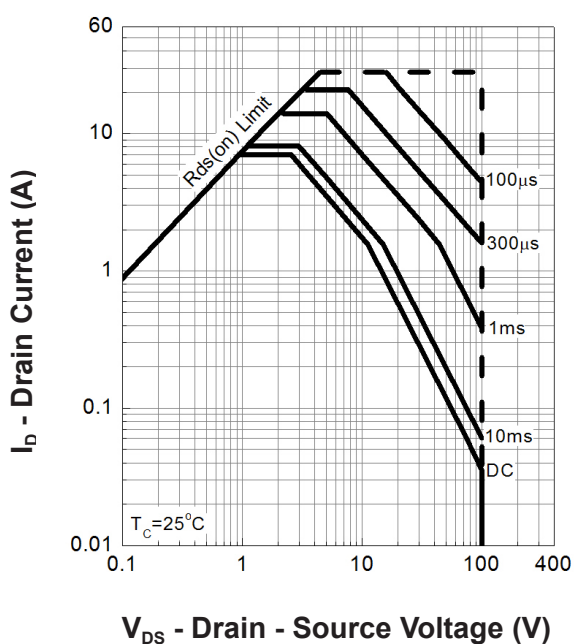
Power Dissipation



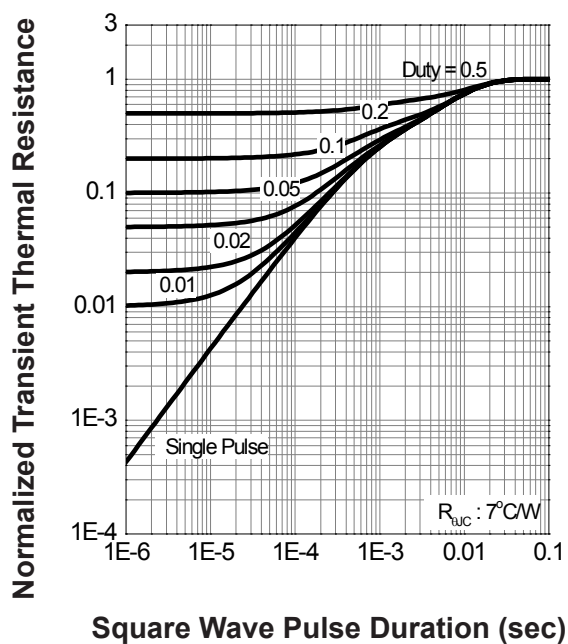
Drain Current



Safe Operation Area

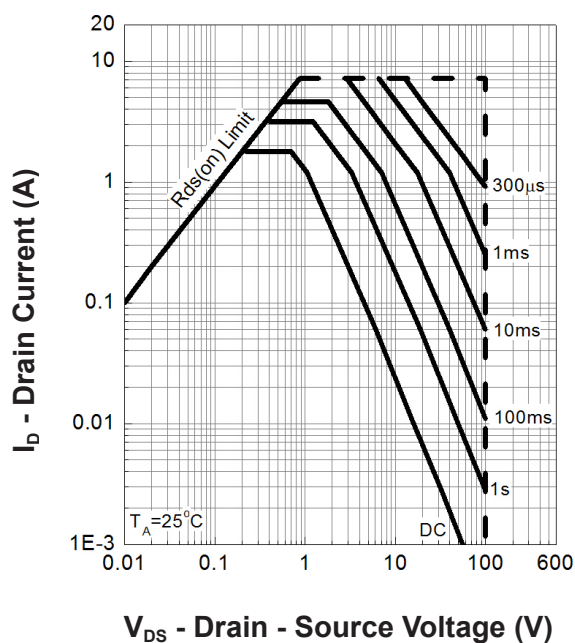


Thermal Transient Impedance

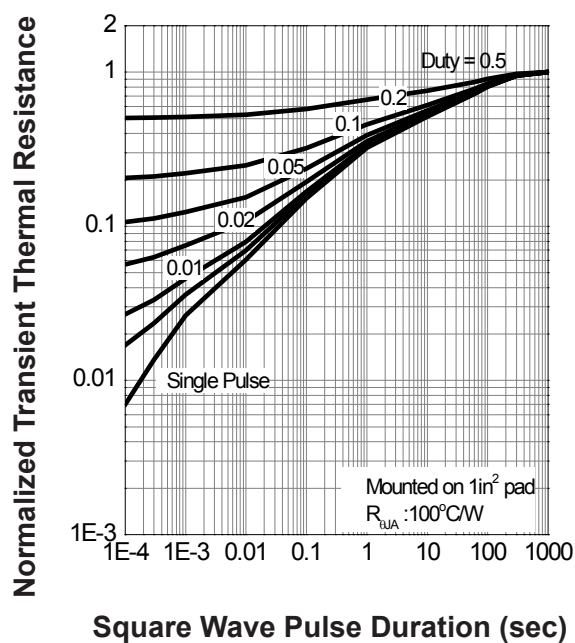


Typical Operating Characteristics(Cont.)

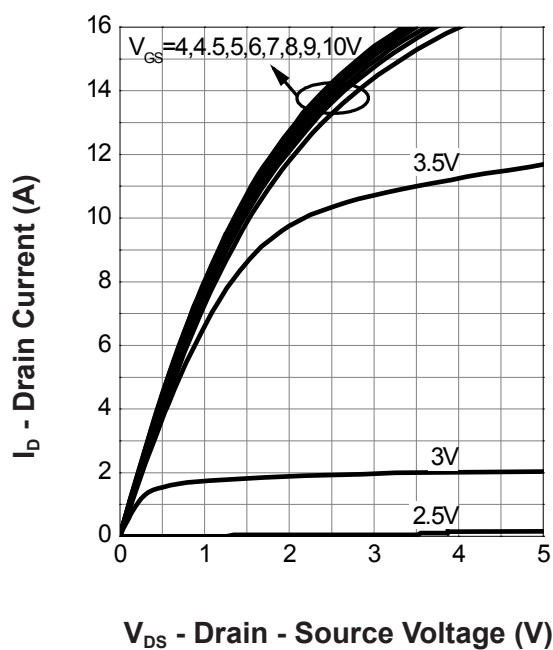
Safe Operation Area



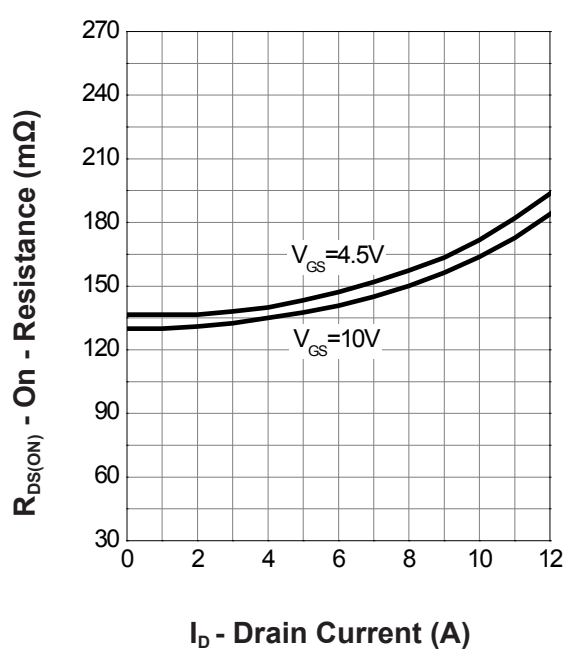
Thermal Transient Impedance



Output Characteristics

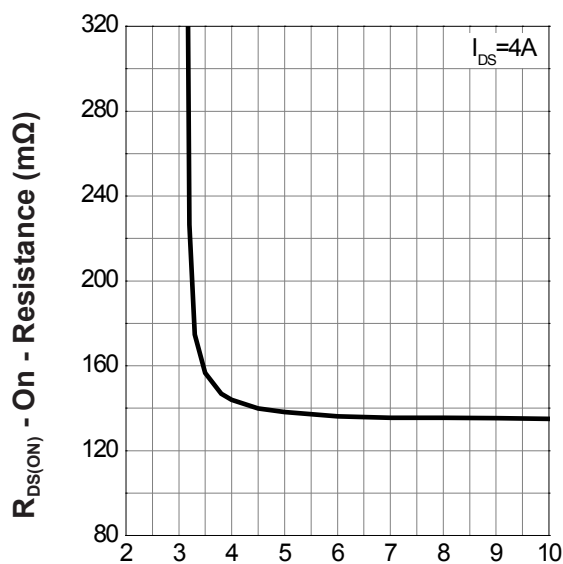


Drain-Source On Resistance

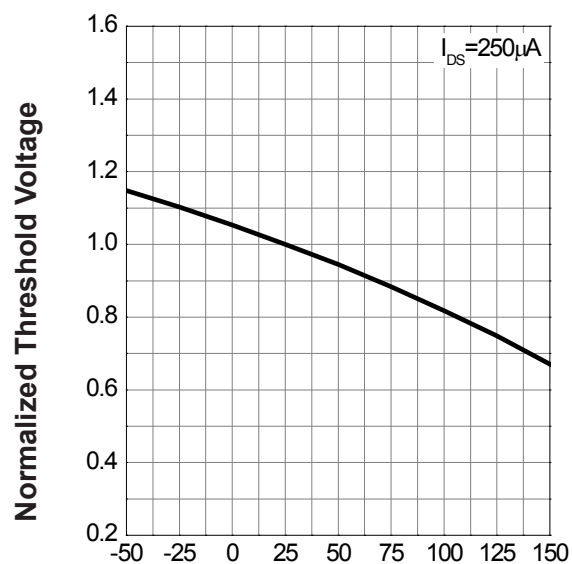


Typical Operating Characteristics(Cont.)

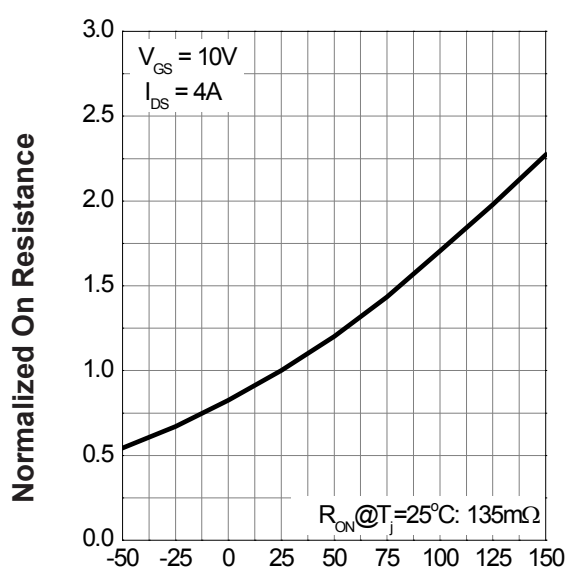
Gate-Source On Resistance

 V_{GS} - Gate - Source Voltage (V)

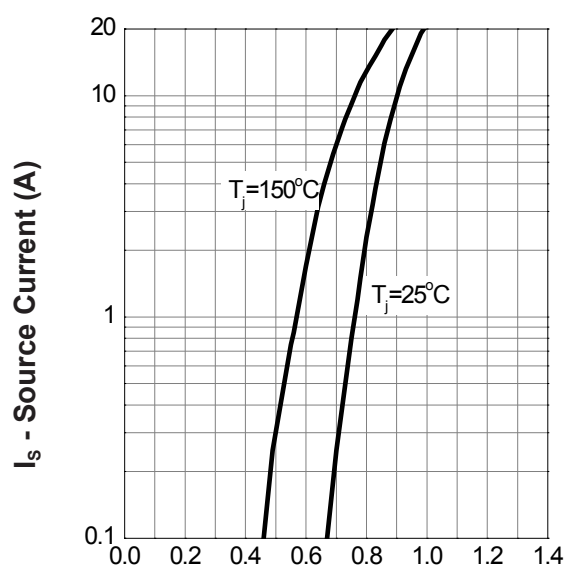
Gate Threshold Voltage

 T_J - Junction Temperature (°C)

Drain-Source On Resistance

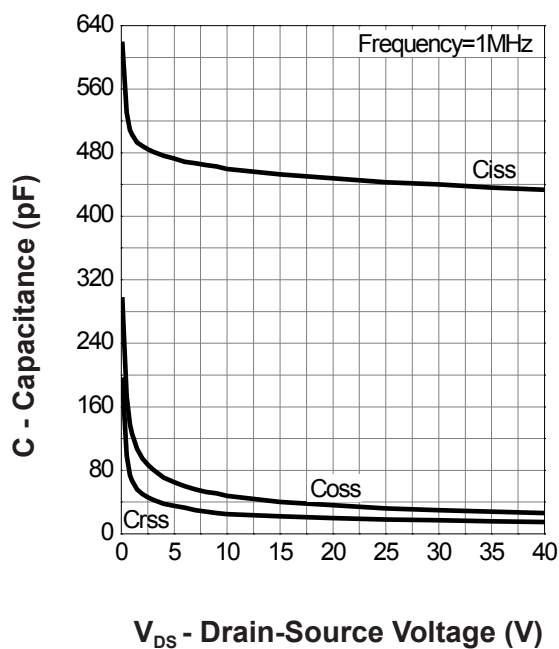
 T_J - Junction Temperature (°C)

Source-Drain Diode Forward

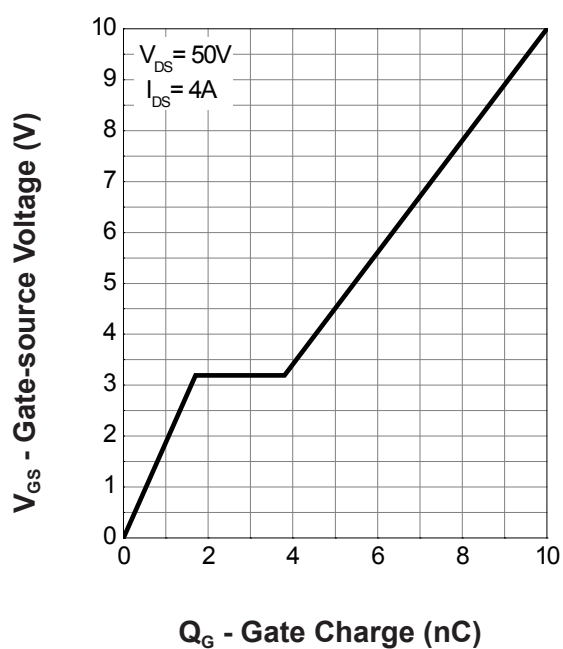
 V_{SD} - Source - Drain Voltage (V)

Typical Operating Characteristics(Cont.)

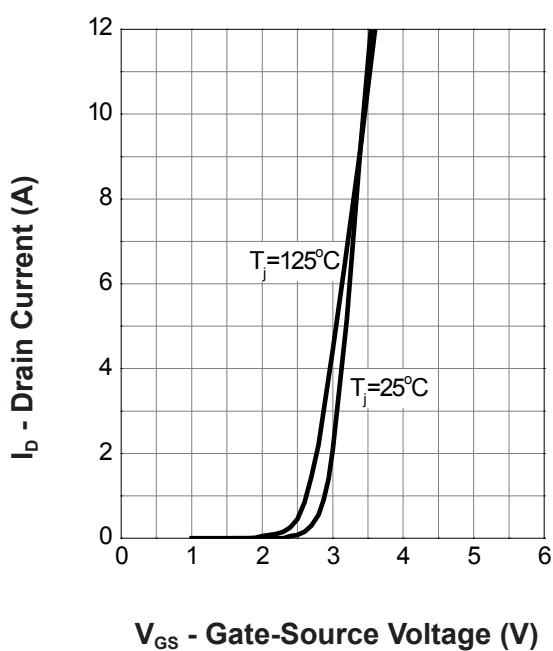
Capacitance



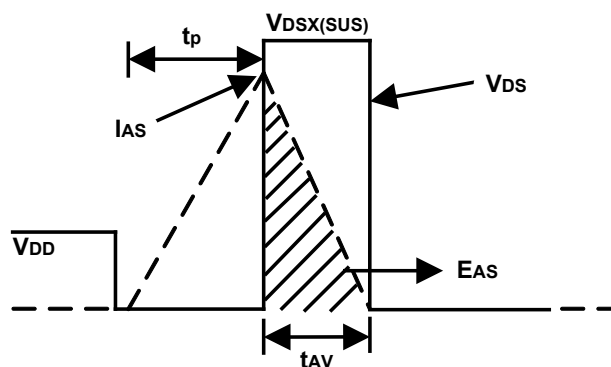
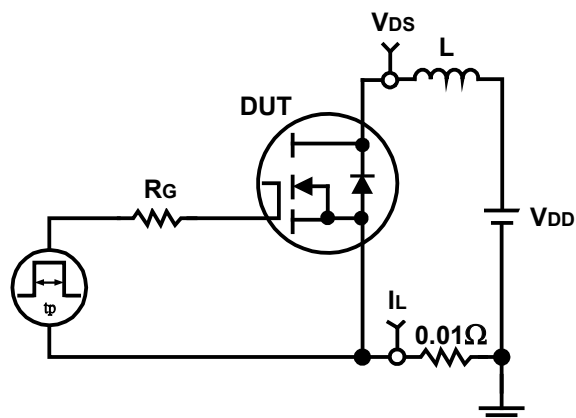
Gate Charge



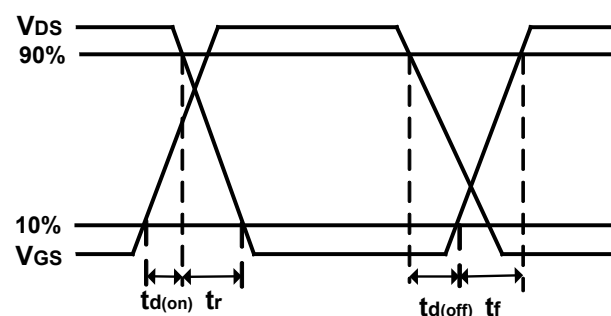
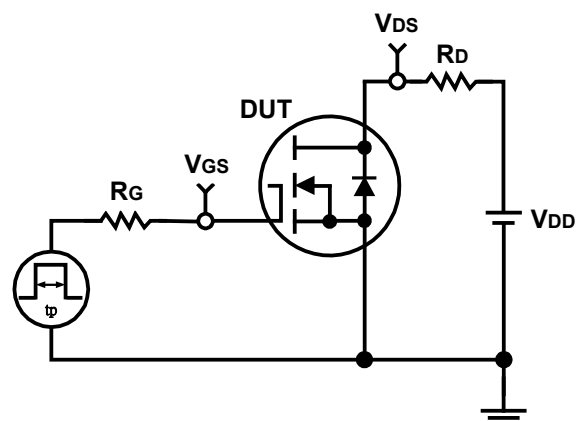
Transfer Characteristics



Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



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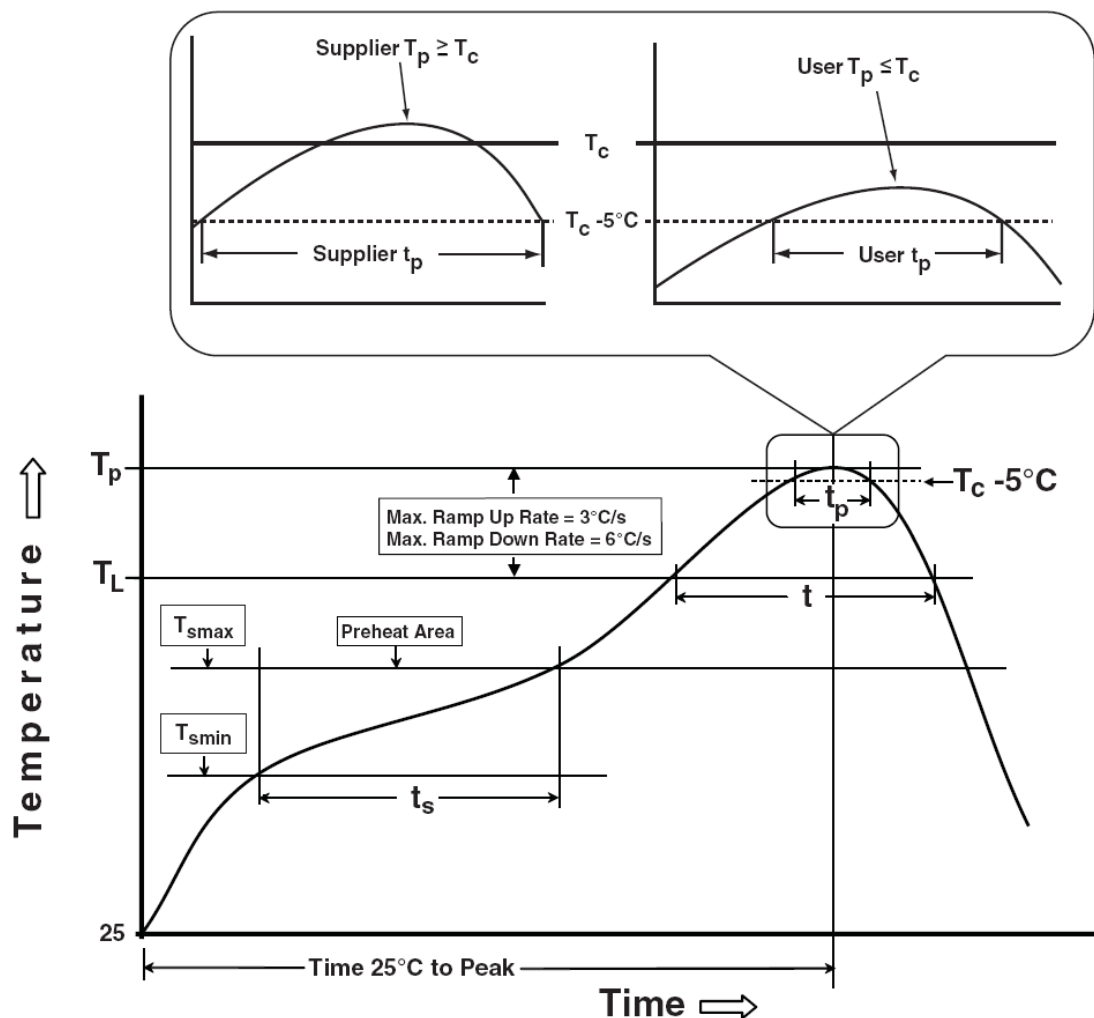
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Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T_{smin})	100 °C	150 °C
Temperature max (T_{smax})	150 °C	200 °C
Time (T_{smin} to T_{smax}) (t_s)	60-120 seconds	60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L)	183 °C	217 °C
Time at liquidous (t_L)	60-150 seconds	60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum.		
** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ T_{jmax}
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ T_{jmax}
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

Customer Service

Sinopower Semiconductor, Inc.

5F, No. 6, Dusing 1St Rd., Hsinchu Science Park,
Hsinchu, 30078, Taiwan
TEL: 886-3-5635818 Fax: 886-3-5635080